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55376 7590 01/05/2006

ROBERT D. VARITZ
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Robert D. Varitz	(Depositor's name)
<i>Robert D. Varitz</i>	(Signature)
January 19, 2006	(Date)

01/24/2006 KBETENM2 00000044 10807931

01 FC:1501
02 FC:15041400.00 OP
300.00 OP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/807,931	03/23/2004	Douglas J. Tweet	SLA0586	5155

TITLE OF INVENTION: METHOD TO FORM LOCAL "SILICON-ON-NOTHING" OR "SILICON-ON-INSULATOR" WAFERS WITH TENSILE-STRAINED SILICON

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	04/05/2006
EXAMINER		ART UNIT	CLASS-SUBCLASS		
LINDSAY JR, WALTER LEE		2812	438-199000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Sharp Laboratories
of America, Inc.

Camas, Washington

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Robert D. Varitz

Date January 19, 2006

Typed or printed name Robert D. Varitz

Registration No 31436

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Douglas James Tweet *et al.*

Customer Number: 55376

Attorney Docket: SLA.0586

Confirmation No: 5155

January 19, 2006

Serial No.: 10/807,931

Group #: 2812

Filed: March 23, 2004

Date of Notice of Allowance: January 5, 2006

For: Method of Forming Local "Silicon-on-Nothing" or "Silicon-on-Insulator" Wafers
with Tensile-strained Silicon

Examiner: Walter Lee Lindsay, Jr.

MS ISSUE FEE c/o TECHNOLOGY CENTER 2800
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Enclosed for filing in the above-identified application are a completed copy of
PTOL Form 85B, and a PTO Form 2038 credit card authorization in the amount of \$1,700.00.

Customer Number

Respectfully Submitted,

55376

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enc.



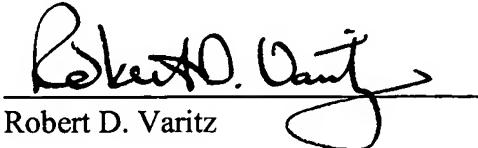
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Robert D. Varitz